

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0



**NETWORK DEVICE, CARRIER MEDIUM AND METHODS FOR
INCREMENTALLY UPDATING A FORWARDING DATABASE THAT IS
SPLIT INTO A BOUNDED NUMBER OF SUB-DATABASES HAVING A
BOUNDED SIZE**

Application Number: 10/809244



Confirmation Number: 6712

First Named Applicant: Pankaj Gupta

Attorney Docket Number: 5298-14001

Search string: (6154384 or 6192051 or 6215816 or 6223172 or 6307855 or
6385649).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
/KL	1	6154384	2000-11-28	Nataraj et al.			
/KL	2	6192051	2001-02-20	Lipman et al.			
/KL	3	6215816	2001-04-10	Gillespie et al.			
/KL	4	6223172	2001-04-04	Hunter et al.			
/KL	5	6307855	2001-10-23	Hariguchi			
/KL	6	6385649	2002-05-07	Draves et al.			

Signature

Examiner Name	Date
/Kimberly Lovel/	04/28/2007

[illegible]

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

/KL/	A1	Lampson et al., "IP Lookup using Multiway and Multicolumn Search," August 1997, pp. 1-23.
/KL/	A2	Gupta, "Algorithms for Routing Lookups and Packet Classification," December 2000, pp. 1-197.

Information Disclosure Statement--PTO 1449 (modified)

<p>Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)</p>	<p>ATTY. DKT. NO. 5298-14200 APPLICANT: Venkatachary et al. FILING DATE: March 28, 2003</p>	<p>SERIAL NO. 10/402,887 GROUP: 2171</p>
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U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

/KL/	B1	Chen et al., "A Fast and Scalable IP Lookup Scheme for High-Speed Networks," © 1999 IEEE, pp. 211-218.
/KL/	B2	Pao et al., "Efficient Hardware Architecture for Fast IP Address Lookup," IEEE INFOCOM 2002, pp. 555-561.
/KL/	B3	Akhbarizadeh et al., "Reconfigurable Memory Architecture for Scalable IP Forwarding Engines," © 2002 IEEE, pp. 432-437.
/KL/	B4	Yu et al., "Forwarding Engine for Fast Routing Lookups and Updates," 1999 Global Telecommunications Conference, pp. 1556-1564
/KL/	B5	International Search Report, PCT/US2004/009531, mailed September 2, 2004.

EXAMINER: /Kimberly Lovel/

DATE CONSIDERED: 04/28/2007

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Information Disclosure Statement--PTO 1449 (modified)

Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT Page 1 of 2	Application No.	10/809,244
	Filed	3/25/2004
	First Inventor	Gupta, Pankaj
	Art Unit	
	Examiner	
	Atty. Docket No.	NLMI.P162

U.S. Patent Documents					
Examiner Initials*	US Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication	Relevant Pages, Columns, Lines
	Number	Kind			
	2002/0089937		Venkatachary et al.	7/1/2002	
	6,018,524		Turner et al.	1/1/2000	

Non Patent Literature Documents		
Examiner Initials	Name of Author, Title of Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country where Published	Trans- lation
/K.L./	A Fast and Scalable IP Lookup Scheme for High-Speed Networks, Chen et al., ©1999 IEEE, pp. 211-218.	
/K.L./	Efficient hardware Architecture for Fast IP Address Lookup, Pao et al., IEEE INFOCOM 2002, PP. 555-561.	
/K.L./	Fast and Scalable Layer 4 Switching, V. Srinivasan, G Varghese, S. Suri and M. Waldvogel, Presented at ACM Sigcomm '98, 16 page slide presentation	
/K.L./	Forwarding Engine for Fast Routing Lookups and Updates, Yu et al., 1999 Global Telecommunications Conference, pp. 1556-1564	
/K.L./	International Search Report, PCT/US2004/009531, Mailed September 2, 2004	

Examiner Signature	/Kimberly Lovel/	Date Considered	04/28/2007
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	First Inventor	Gupta, Pankaj
	Art Unit	
	Examiner	
	Atty. Docket No.	NLMI.P162

Non Patent Literature Documents		
Examiner Initials	Name of Author, Title of Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country where Published	Trans- lation
/K.L.	Packet Classification using hierarchical Intelligent Cuttings, Pankaj Gupta and Nick McKeown, Proc. Hot Interconnects VII, August 1999, Stanford University	
/K.L./	Packet Classification on Multiple Fields, Pankj Gupta and Nick McKeown, Proc. Sigcomm, Computer Communication Review, Vol 29, no. 4, pp. 147-60, September 1999 Harvard University	
/K.L./	Reconfigurable memory Architecture for Scalable IP Forwarding Engines, Akhbarizadeh et al., © 2002 IEEE, pp. 432-437.	

Examiner Signature	/Kimberly Lovel/	Date Considered	04/28/2007
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